



Class	Subclass

ISSUE CLASSIFICATION

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PATENT NUMBER

U.S. UTILITY Patent Application

<div>SCANNED</div> <div><div>Q.I.P.E.</div><div>Q.A.</div></div>	<div>PATENT DATE</div>
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APPLICATION NO. 097681103	CONT/PRIOR	CLASS 716 703	SUBCLASS 24	ART UNIT 2025 2123	EXAMINER Jones Bridget HOGAN
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[illegible]

3. Logic emulator with routing chip providing virtual full-crossbar interconnect

PTO-2040
12/89[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____				
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